

WEST Search History

DATE: Wednesday, April 11, 2007

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L56	port\$1 with L55	43
<input type="checkbox"/>	L55	bank\$1 with register\$1	1998
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L54	tetsuya.in. and l53	0
<input type="checkbox"/>	L53	morita.in. and nec.as.	66
<input type="checkbox"/>	L52	morita.in. and nec.as. and size.ti.	0
	<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L51	l46 and L50	1
<input type="checkbox"/>	L50	l44 or l45	2
<input type="checkbox"/>	L49	64-bit port\$1	0
<input type="checkbox"/>	L48	64 bit port\$1	0
<input type="checkbox"/>	L47	32-bit port\$1	3
<input type="checkbox"/>	L46	32 bit port\$1	3
<input type="checkbox"/>	L45	16 bit port\$1	2
<input type="checkbox"/>	L44	16-bit port\$1	1
<input type="checkbox"/>	L43	16-bit port	1
<input type="checkbox"/>	L42	variable bit width	10
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L41	variable bit width	84
<input type="checkbox"/>	L40	L24 and L25	3
<input type="checkbox"/>	L39	L23 and L25	1
<input type="checkbox"/>	L38	L30 and L36	4
<input type="checkbox"/>	L37	L14 and L36	1
<input type="checkbox"/>	L36	L23 and L24	37
<input type="checkbox"/>	L35	L27 and L25	1
<input type="checkbox"/>	L34	L30 and L32	3
<input type="checkbox"/>	L33	L14 and L32	0
<input type="checkbox"/>	L32	L27 and L24	15
<input type="checkbox"/>	L31	L28 and L30	15
<input type="checkbox"/>	L30	bank\$1 with register\$1	9510
<input type="checkbox"/>	L29	L14 and L28	1

<input type="checkbox"/>	L28	L23 and L27	44
<input type="checkbox"/>	L27	8-bit port\$1	354
<input type="checkbox"/>	L26	128-bit port\$1	3
<input type="checkbox"/>	L25	64-bit port\$1	37
<input type="checkbox"/>	L24	32-bit port\$1	82
<input type="checkbox"/>	L23	16-bit port\$1	306
<input type="checkbox"/>	L22	theis.in. and data path architectures	0
<input type="checkbox"/>	L21	port\$1 and L20	0
<input type="checkbox"/>	L20	5754875.pn.	1
<input type="checkbox"/>	L19	port\$1 and L18	42
<input type="checkbox"/>	L18	data type\$1 and L17	69
<input type="checkbox"/>	L17	(712/225).ccls.	629
<input type="checkbox"/>	L16	data type\$1 and L14	22
<input type="checkbox"/>	L15	data type\$1 with L14	0
<input type="checkbox"/>	L14	(711/149).ccls.	393
<input type="checkbox"/>	L13	data type\$1 with L3	0
<input type="checkbox"/>	L12	L3 and L11	0
<input type="checkbox"/>	L11	input port\$1 with data type\$1	61
<input type="checkbox"/>	L10	L7 same L8	3
<input type="checkbox"/>	L9	L7 same L5	0
<input type="checkbox"/>	L8	input port\$1 with floating point	42
<input type="checkbox"/>	L7	input port\$1 with integer	546
<input type="checkbox"/>	L6	L4 same L5	162
<input type="checkbox"/>	L5	port\$1 with floating point	473
<input type="checkbox"/>	L4	port\$1 with integer	2358
<input type="checkbox"/>	L3	6343356.uref.	5
<input type="checkbox"/>	L2	port\$1 and L1	1
<input type="checkbox"/>	L1	6343356.pn.	1

END OF SEARCH HISTORY